

8		7		6		5		4		3		2		1			
1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%. 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS. 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ. SCHEM, DOB, Q41A (DAUGHTER OF BUBBA) 12/15/2003												REV	ZONE	ECN	DESCRIPTION OF CHANGE	CK APPD DATE	ENG APPD DATE
												01		308001	ENGINEERING RELEASED	12/19/03	?
PAGE 1 2 3 4 5 6 7 CONTENTS TITLE PAGE AND CONTENTS PCB NOTES AND HOLES BACK UP BATTERY RIGHT USB PORT CONSTRAINTS / REVISION HISTORY SIGNAL LOCATIONS COMPONENT LOCATIONS																	
PART# QTY DESCRIPTION REFERENCE DESIGNATOR(S) BOM OPTION 051-6601 1 SCHEM, DOB, Q41A SCH1 820-1618 1 PCBF, DOB, Q41A PCB1 DIMENSIONS ARE IN MILLIMETERS XX ± _____ X.XX ± _____ X.XXX ± _____ ANGLES ± _____ DO NOT SCALE DRAWING THIRD ANGLE PROJECTION METRIC DRAFTER ENG APPD QA APPD RELEASE DESIGN CK MFG APPD DESIGNER SCALE MATERIAL/FINISH NOTED AS APPLICABLE SIZE D Apple Computer Inc. NOTICE OF PROPRIETARY PROPERTY THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE COMPUTER, INC. THE POSSESSOR AGREES TO THE FOLLOWING I TO MAINTAIN THE DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IN WHOLE OR PART TITLE SCHEM, DOB, Q41A DRAWING NUMBER 051-6601 REV. 01 SHT 1 OF 5																	
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PCB SPECS

THICKNESS : 1.2 MM / 0.047 IN
1/2 OZ CU THICKNESS: 0.7 MILS
1.0 OZ CU THICKNESS: 1.4 MILS

IMPEDANCE : 50 OHMS +/- 10%
DIELECTRIC: FR-4
LAYER COUNT: 12
SIGNAL TRACE WIDTH: 4 MILS
SIGNAL TRACE SPACING: 4 MILS
PREPREG THICKNESS: 2-3 MILS

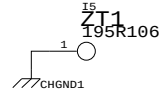
SEE PCB CAD FILES FOR MORE SPECIFIC INFO.

BOARD STACK-UP AND CONSTRUCTION

20R10 TH VIA OR VIA IN PAD

1		SIGNAL (1/3 OZ + COPPER PLATING)
2	PREPREG (3MIL)	GROUND (1/2 OZ)
3	LAMINATE (4MIL)	SIGNAL (1/2 OZ)
4	PREPREG (3MIL)	SIGNAL (1/2 OZ)
5	LAMINATE (4MIL)	GROUND (1/2 OZ)
6	PREPREG (2MIL)	CUT POWER PLANE(1 OZ)
7	LAMINATE (3MIL)	CUT POWER PLANE(1 OZ)
8	PREPREG (2MIL)	GROUND (1/2 OZ)
9	LAMINATE (4MIL)	SIGNAL (1/2 OZ)
10	PREPREG (3MIL)	SIGNAL (1/2 OZ)
11	LAMINATE (4MIL)	GROUND (1/2 OZ)
12	PREPREG (3MIL)	SIGNAL (1/3 OZ + COPPER PLATING)

BOARD HOLES



PCB BOARD STANDOFFS

BOARD INFORMATION

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051-6601

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SCALE

NONE

SHT

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The schematic diagram illustrates the internal components of the USB Right Channel (URC). Key components include:

- U1 MIC2025 MSOP:** A differential line driver/receiver. Its EN pin is connected to USB_RIGHT_EN_A. Its IN and OUT pins are connected to the USB_D2M and USB_D2P signals. Its FLG* pin is connected to USB_RIGHT_FLG_A. Its NC pins are connected to GND. Its 5_NC pin is connected to the +5V_USB_VDD_RIGHT supply.
- J1 UB1193-N1301 F-R-T-TH:** A 1:1 transformer. Its primary is connected to the +5V_USB_VDD_RIGHT supply and the +5V_USB_VDD_RIGHT_EMI signal. Its secondary is connected to the USB_D2M and USB_D2P signals.
- Capacitors:**
 - C1, C10:** 10µF and 0.1µF capacitors, respectively, connected to the +5V_MAIN supply.
 - C2, C25:** 10µF and 100µF capacitors, respectively, connected to the +5V_USB_VDD_RIGHT supply.
 - C6, C7, C8:** 0.01µF capacitors, respectively, connected to the USB_D2M and USB_D2P signals.
 - C9:** A 12PF capacitor connected to the USB_D2M and USB_D2P signals.
- Inductors:**
 - L2:** A 165-ohm inductor connected to the USB_D2M and USB_D2P signals.
 - L4, L5:** Ferrite beads (FERR-250-OHM) connected to the +5V_USB_VDD_RIGHT and +5V_USB_VDD_RIGHT_EMI signals.
- Resistors:**
 - R9, R8:** 15K resistors connected to the USB_D2M and USB_D2P signals.

The diagram is labeled 'CRITICAL' in several places, indicating that the components and their connections are critical for the proper operation of the URC.

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NONE		4	5	

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